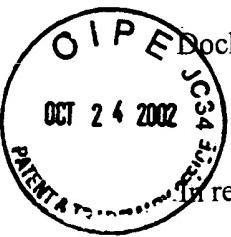


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Docket No.: 57454-011

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of  
Akira YAMAZAKI, et al.

Serial No.: 09/780,477

Filed: February 12, 2001

For: MULTI-POWER SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

: **RESPONSE UNDER 37 CFR 1.116**  
: **EXPEDITED PROCEDURE**

:  
:  
: Group Art Unit: 2816  
:  
: Examiner: A. Q. TRA  
:

Box AF  
Commissioner for Patents  
Washington, DC 20231

Sir:

Reconsideration of the final Office Action dated July 26, 2002 is respectfully solicited in light of the following remarks.

**REMARKS**

Claims 1-20 are pending in this application, of which claims 1, 11, 19, and 20 are independent. For the following reasons, this application should be considered in condition for allowance and passed to issue.

In response to the Amendment filed on June 24, 2002, the Examiner maintains the rejection of claims 1-3, 7, and 9 under 35 U.S.C. §102(e) as being anticipated by Crotty (U.S. Patent No. 6,160,431), and maintains the rejection of claims 11, 16, 18, and 20 under 35 U.S.C. §103(a) as being unpatentable over the same reference. The Examiner continues to maintain the allowability of claims 4-6, 8-10 and 12-15 if rewritten in independent form.